

Searching PAJ

Page 1 of 2

PATENT ABSTRACTS OF JAPAN

(11) Publication number : 07-154893
 (43) Date of publication of application : 16.06.1995

(51) Int.Cl.

H04R 5/02
 G10K 15/00
 H04R 1/40

(21) Application number : 05-301465

(22) Date of filing : 01.12.1993

(71) Applicant : NIPPON HOSO KYOKAI <NHK>

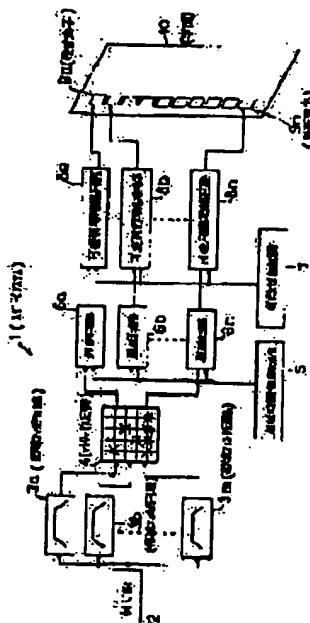
(72) Inventor : HARAGA TOSHIAKI
 MORITA AKIRA

(54) SPEAKER SYSTEM

(57) Abstract:

PURPOSE: To freely control acoustic characteristics and to control the sound field of the speaker of an optional surface by performing a prescribed signal processing to sound signals, generating the plural sound signals, fetching them by sounding elements arranged on an optional shape surface and reproducing sound.

CONSTITUTION: This speaker system 1 is provided with a signal source 2, plural band filter circuits 3a-3m, a matrix circuit 4, a delay time control circuit 5, plural delay circuits 6a-6n, a gain control circuit 7, variable gain amplifier circuits 8a-8n and the sounding elements 9a-9n. Then, (the sound signals of one channel or plural channels) generated by the signal source 2 are filtered, turned to the sound signals of plural bands and distributed to the sounding elements 9a-9n. Then, a delay amount and a gain are controlled for the respective sounding elements 9a-9n, they are supplied to the sounding elements 9a-9n and output is performed. The sounding elements 9a-9n are chip elements and arranged in a matrix on a plane 10 and the output is performed through the variable gain amplifier circuits 8a-8n.



LEGAL STATUS

[Date of request for examination] 03.04.2000

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision]